

IN THE SPECIFICATION

**Amendments to the Specification:**

The following amendments to specification are inserted to comply with the objections raised by the Examiner. The material being inserted is the material incorporated by reference and the amendment to specification contains no new matter.

***Please amend the paragraph beginning on page 1, lines 6-9 with the following:***

This application is related to U.S. Patent Application No. 10/645,789 (~~Attorney Docket No. ADAPP243~~), filed on the same day as the instant application and entitled “INTEGRATED CIRCUIT VERIFICATION SCHEME.” This application is hereby incorporated by reference in its entirety.

***Please amend the paragraph beginning on page 9, lines 1-9 with the following:***

Figure 2A is a simplified schematic diagram of the components of a client configured to enable acceleration of test case development time in accordance with one embodiment of the invention. In one embodiment, the system of Figure 2A is a general computer system. Client 102 includes processor 110, memory 108 and storage media 114. Bus 112 enables communication between processor 110, memory 108 and storage media 114. In one embodiment, storage media 114 includes precompiled files 116 as discussed with reference to Application Serial No. 10/645,789 (~~Attorney Docket No. ADAP243~~) which has been incorporated by reference. In another embodiment, precompiled files 116 are verilog tasks.

***Please amend the paragraph beginning on page 10, lines 13-24 and page 11, lines 1-4 with the following:***

The testinput.txt file of block 124 of Figure 3 is a pre-processed file in which syntax and format errors associated with the testcase.txt file have been checked. Thus, a verified file is generated that is then moved to the data stream where the server component 142 is running. In one embodiment, testinput.txt of block 124 includes text-based tasks associated with precompiled hardware description language based tasks. In one embodiment, a translation layer correlates the text based tasks to hardware description language based tasks. Further details of the pre-processing operation, the translation operation, the text based tasks and the precompiled tasks are contained in Application Serial No. 10/645,789 (Attorney Docket No. ADAP243) which has been incorporated by reference. Server component 142 maintains the server in an initialized state as mentioned above. Here, chip initialization text of block 130 is executed by a simulation server. The simulation server executes the re-set and chip initialization sequence in block 132. The initialized server is maintained in the initialized state, awaiting for a test case to be delivered from client side 144 as illustrated in block 134. Thus, when the testinput.txt file block 124 is moved into the data stream of the server component 142, the simulation server is available to immediately process and execute the test case associated with the testinput.txt file. Accordingly, the simulation server executes the test and publishes the test case results in block 136.